

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Visokay et al.

Art Unit: 2825

Serial No.: 10/661,130

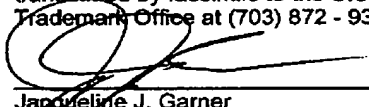
Examiner: Malsawma, L.

Filing Date: 09/12/2003

Docket No.: TI-35942

Customer No.: 23494

Conf. No.: 7761

Title: METAL GATE MOS TRANSISTORS AND METHODS FOR
MAKING THE SAME**RECEIVED**
CENTRAL FAX CENTER
FEB 02 2005**AMENDMENT UNDER 37 CFR 1.111**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**CERTIFICATION OF FACSIMILE TRANSMISSION**I hereby certify that the following papers are being
transmitted by facsimile to the U.S. Patent and
Trademark Office at (703) 872 - 9306 on 2/2/2005.
Jacqueline J. Garner
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 08/05/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 8.

To: Technology Center 2800

Total Pages Sent 14

From: Jacqueline J. Garner
Texas Instruments Incorporated
Phone: 214-532-9348

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I hereby certify that the following papers are being transmitted by facsimile to the
U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:
Jacqueline J. Garner, Reg. No. 36,144February 2, 2005
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (9 Pages)
<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT 3EOT(2 Pages)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL (# Pages)
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE (# Pages)
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (IN TRIPLICATE) (# Pages)
<input type="checkbox"/> CONTINUATION APP'N (# Pages)	<input checked="" type="checkbox"/> CLAIM FEE
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Visokay	
TITLE OF INVENTION: Metal Gate MOS Transistors . . .	
TI FILE NO.: TI-35942	DEPOSIT ACCT. NO.: 20-0668
FAXED: 2/2/2005 DUE: 11/5/2004 ATTY/SEC'Y: JJG	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/661,130 Filing Date: 09/12/2003 Conf. No.: 7761	

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75074

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Title: METAL GATE MOS TRANSISTORS AND METHODS FOR MAKING THE SAME

Amendment Fee

Assistant Commissioner of Patents

P. O. Box 1450

Alexandria, VA 22313-1450

February 2, 2005

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Jacqueline J. Garner, Reg. No. 36,144

February 2, 2005
Date

Transmitted herewith is an amendment in the above-identified Application. The Fee has been calculated as shown below.

CLAIMS AS AMENDED						
	Claims remaining after amendment		Highest Number Previously Paid for	Present Extra	Rate	Additional Fee
TOTAL	30	Minus	35	= 0	x \$50	\$ 00.00
Independent Claims	5	Minus	3	= 2	x \$200	\$ 400.00
Total Additional Fee for this Amendment						\$ 400.00

Charge the total additional fee, and any further fees, or credit any overpayment to Deposit Account No. 20-0668.

Respectfully Submitted,

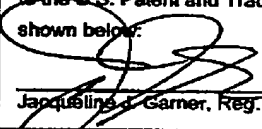
Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265Jacqueline J. Garner
Reg. No. 36,144

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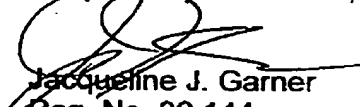
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 Jacqueline J. Garner
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